

**Sr.No.**  
**983405136**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1831606

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 1ST SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2014-2015

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name		Maximum Marks		Total	Marks Obtained		Total	Result		
		Internal	External		Internal	External				
[BTEC-101] ENGINEERING MATHEMATICS-I		30	70	100	19	47	66	P		
[BTEC-102] ENGINEERING PHYSICS-I		30	70	100	21	45	66	P		
[BTEC-103] ENGINEERING CHEMISTRY		30	70	100	19	47	66	P		
[BTEC-104] ENGINEERING GRAPHICS		30	70	100	22	36	58	P		
[BTEC-105] MANUFACTURING PROCESSES		30	70	100	22	35	57	P		
[BTEC-106] ELECTRICAL TECHNOLOGY		30	70	100	19	47	66	P		
[BTEC-107] ELECTRONIC CIRCUITS LAB-I		30	70	100	18	55	73	P		
TOTAL		210	490	700	140	312	452	RMK	64.57%	
SEMESTER	FIRST	SECOND	THIRD	FOURTH	FIFTH	SIXTH	SEVENTH	EIGHTH	TOTAL	%
MAX.MARKS	700	*	*	*	*	*	*	*	700	
TOTAL	452	*	*	*	*	*	*	*	452	64.57%

**Note : Abbreviations**

**P**-Pass  
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**F**-Fail  
**Q**-Qualified

**NC**-Not Completed  
**NQ**-Not Qualified

**G**-Grace Marks  
**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**

**Sr.No.**  
**008194588**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1936926

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 2ND SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2014-2015

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name		Maximum Marks		Total	Marks Obtained		Total	Result		
		Internal	External		Internal	External				
[BTEC-201] ENGINEERING MATHEMATICS-II		30	70	100	18	44	62	P		
[BTEC-202] ENGINEERING PHYSICS-II		30	70	100	21	57	78	P		
[BTEC-203] FUNDAMENTALS OF COMPUTING		30	70	100	18	41	59	P		
[BTEC-204] ENGINEERING MECHANICS		30	70	100	23	49	72	P		
[BTEC-205] SOLID STATE DEVICES AND CIRCUITS		30	70	100	24	44	68	P		
[BTEC-206] ENGINEERING MATERIAL SCIENCE		30	70	100	17	58	75	P		
[BTEC-207] ELECTRONIC CIRCUITS LAB-II		30	70	100	20	59	79	P		
<b>TOTAL</b>		<b>210</b>	<b>490</b>	<b>700</b>	<b>141</b>	<b>352</b>	<b>493</b>	<b>RMK</b>	<b>70.43%</b>	
<b>SEMESTER</b>	<b>FIRST</b>	<b>SECOND</b>	<b>THIRD</b>	<b>FOURTH</b>	<b>FIFTH</b>	<b>SIXTH</b>	<b>SEVENTH</b>	<b>EIGHTH</b>	<b>TOTAL</b>	<b>%</b>
<b>MAX.MARKS</b>	<b>700</b>	<b>700</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>1400</b>	
<b>TOTAL</b>	<b>452</b>	<b>493</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>*</b>	<b>945</b>	<b>67.50%</b>

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**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**

**Sr.No.**  
**192591664**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1530563

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 3RD SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2015-2016

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name	Maximum Marks		Total	Marks Obtained		Total	Result			
	Internal	External		Internal	External					
[BTEC-301] ELECTRONICS CIRCUITS, ANALYSIS & DESIGN	30	70	100	24	42	66	P			
[BTEC-302] ELECTRICAL NETWORKS ANALYSIS & SYNTHESIS - I	30	70	100	17	35	52	P			
[BTEC-303] ELECTRONICS DEVICES & CIRCUITS	30	70	100	18	43	61	P			
[BTEC-304] ELECTRICAL MEASUREMENTS & MEASURING INSTRUMENTS	30	70	100	22	53	75	P			
[BTEC-305] SWITCHING THEORY	30	70	100	22	45	67	P			
[BTEC-306] PULSE & DIGITAL ELECTRONICS	30	70	100	19	60	79	P			
[BTEC-307] PULSE & DIGITAL ELECTRONICS LAB	30	70	100	17	52	69	P			
<b>TOTAL</b>		210	490	700	139	330	469	<b>RMK</b>	<b>67.00%</b>	
<b>SEMESTER</b>	<b>FIRST</b>	<b>SECOND</b>	<b>THIRD</b>	<b>FOURTH</b>	<b>FIFTH</b>	<b>SIXTH</b>	<b>SEVENTH</b>	<b>EIGHTH</b>	<b>TOTAL</b>	<b>%</b>
<b>MAX.MARKS</b>	700	700	700	*	*	*	*	*	2100	
<b>TOTAL</b>	452	493	469	*	*	*	*	*	1414	67.33%

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**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**

**Sr.No.**  
**145680026**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1606134

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 4TH SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2015-2016

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name		Maximum Marks		Total	Marks Obtained		Total	Result		
		Internal	External		Internal	External				
[BTEC-401] SIGNALS & SYSTEMS		30	70	100	17	54	71	P		
[BTEC-402] ELECTROMAGNETIC FIELD THEORY		30	70	100	18	39	57	P		
[BTEC-403] ELECTRICAL NETWORKS ANALYSIS & SYNTHESIS - II		30	70	100	17	55	72	P		
[BTEC-404] ELECTRICAL & ELECTRONICS ENGG. MATERIALS		30	70	100	15	36	51	P		
[BTEC-405] ENVIRONMENTAL SCIENCE & ENGG.		30	70	100	20	46	66	P		
[BTEC-406] MICROPROCESSORS & APPLICATIONS		30	70	100	18	38	56	P		
TOTAL		180	420	600	105	268	373	RMK	62.17%	
SEMESTER	FIRST	SECOND	THIRD	FOURTH	FIFTH	SIXTH	SEVENTH	EIGHTH	TOTAL	%
MAX.MARKS	700	700	700	600	*	*	*	*	2700	
TOTAL	452	493	469	373	*	*	*	*	1787	66.19%

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**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**

**Sr.No.**  
**043473847**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1794840

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 5TH SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2016-2017

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name	Maximum Marks		Total	Marks Obtained		Total	Result			
	Internal	External		Internal	External					
[BTEC-501] PRINCIPLES OF COMMUNICATION	30	70	100	16	58	74	P			
[BTEC-502] ELECTRO MECHANICAL ENERGY CONVERSION	30	70	100	16	41	57	P			
[BTEC-503] ANALOG INTEGRATED CIRCUITS	30	70	100	15	43	58	P			
[BTEC-504] AUTOMATIC CONTROL SYSTEM	30	70	100	16	36	52	P			
[BTEC-505] ANTENNA & WAVE PROPAGATION	30	70	100	16	37	53	P			
[BTEC-506] COMMUNICATION ENGINEERING	30	70	100	15	57	72	P			
TOTAL		180	420	600	94	272	366	RMK	61.00%	
SEMESTER	FIRST	SECOND	THIRD	FOURTH	FIFTH	SIXTH	SEVENTH	EIGHTH	TOTAL	%
MAX.MARKS	700	700	700	600	600	*	*	*	3300	
TOTAL	452	493	469	373	366	*	*	*	2153	65.24%

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**NQ**-Not Qualified

**G**-Grace Marks  
**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**

**Sr.No.**  
**103156665**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1576086

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 6TH SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2016-2017

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name		Maximum Marks		Total	Marks Obtained		Total	Result		
		Internal	External		Internal	External				
[BTEC-601] POWER SYSTEM ANALYSIS		30	70	100	20	42	62	P		
[BTEC-602] POWER ELECTRONICS		30	70	100	17	40	57	P		
[BTEC-603] VLSI-TECHNOLOGY & DESIGN		30	70	100	17	47	64	P		
[BTEC-604] INDUSTRIAL MANAGEMENT		30	70	100	23	38	61	P		
[BTEC-605] ANALOG & DIGITAL COMMUNICATION ENGINEERING		30	70	100	17	39	56	P		
[BTEC-606] MICROWAVE RADAR ENGINEERING		30	70	100	17	53	70	P		
<b>TOTAL</b>		<b>180</b>	<b>420</b>	<b>600</b>	<b>111</b>	<b>259</b>	<b>370</b>	<b>RMK</b>	<b>61.67%</b>	
<b>SEMESTER</b>	<b>FIRST</b>	<b>SECOND</b>	<b>THIRD</b>	<b>FOURTH</b>	<b>FIFTH</b>	<b>SIXTH</b>	<b>SEVENTH</b>	<b>EIGHTH</b>	<b>TOTAL</b>	<b>%</b>
<b>MAX.MARKS</b>	<b>700</b>	<b>700</b>	<b>700</b>	<b>600</b>	<b>600</b>	<b>600</b>	<b>*</b>	<b>*</b>	<b>3900</b>	
<b>TOTAL</b>	<b>452</b>	<b>493</b>	<b>469</b>	<b>373</b>	<b>366</b>	<b>370</b>	<b>*</b>	<b>*</b>	<b>2523</b>	<b>64.69%</b>

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**G**-Grace Marks  
**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**

**Sr.No.**  
**840221635**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1258161

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 7TH SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2017-2018

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name		Maximum Marks		Total	Marks Obtained		Total	Result		
		Internal	External		Internal	External				
[BTEC-701] SWITCH GEAR & PROTECTION		30	70	100	17	49	66	P		
[BTEC-702] ELECTRONICS SWITCHING		30	70	100	24	57	81	P		
[BTEC-703] POWER SYSTEM OPERATION AND CONTROL		30	70	100	23	55	78	P		
[BTEC-704] TELEMETRY OF DATA TRANSMISSION		30	70	100	18	43	61	P		
[BTEC-705] OPTOELECTRONICS		30	70	100	15	51	66	P		
[BTEC-706] OPTICAL INSTRUMENTATION		30	70	100	17	41	58	P		
<b>TOTAL</b>		<b>180</b>	<b>420</b>	<b>600</b>	<b>114</b>	<b>296</b>	<b>410</b>	<b>RMK</b>	<b>68.33%</b>	
<b>SEMESTER</b>	<b>FIRST</b>	<b>SECOND</b>	<b>THIRD</b>	<b>FOURTH</b>	<b>FIFTH</b>	<b>SIXTH</b>	<b>SEVENTH</b>	<b>EIGHTH</b>	<b>TOTAL</b>	<b>%</b>
<b>MAX.MARKS</b>	<b>700</b>	<b>700</b>	<b>700</b>	<b>600</b>	<b>600</b>	<b>600</b>	<b>600</b>	<b>*</b>	<b>4500</b>	
<b>TOTAL</b>	<b>452</b>	<b>493</b>	<b>469</b>	<b>373</b>	<b>366</b>	<b>370</b>	<b>410</b>	<b>*</b>	<b>2933</b>	<b>65.18%</b>

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**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**

**Sr.No.**  
**048969516**

**Enrollment No.** : MU1418B556302

**Roll No.** : 1792661

**Candidate's Name** : ANURAG BHUSHAN VERMA

**Year / Semester** : 8TH SEMESTER

**Father's Name** : P LAL VERMA

**Session** : 2017-2018

**Course** : B.TECH. [ELECTRONICS & COMMUNICATION ENGG.]

Subject Name		Maximum Marks		Total	Marks Obtained		Total	Result		
		Internal	External		Internal	External				
[BTEC-801] INSTRUMENTATION AND PROCESS CONTROL		30	70	100	24	46	70	P		
[BTEC-802] UTILIZATION OF ELECTRIC ENERGY & TRACTION		30	70	100	16	44	60	P		
[BTEC-803] COMPUTER NETWORKS		30	70	100	21	40	61	P		
[BTEC-804] BIOINSTRUMENTATION		30	70	100	18	54	72	P		
[BTEC-805] PROJECT		30	70	100	21	43	64	P		
TOTAL		150	350	500	100	227	327	RMK	65.40%	
SEMESTER	FIRST	SECOND	THIRD	FOURTH	FIFTH	SIXTH	SEVENTH	EIGHTH	TOTAL	%
MAX.MARKS	700	700	700	600	600	600	600	500	5000	
TOTAL	452	493	469	373	366	370	410	327	3260	65.20%

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**RMK**-Remarks

**Date of Issue :**

**Prepared By**

**Checked By**

**Controller of Examinations**