

Transcript

ENROLLMENT NO. : MU1519B094559
STUDENT NAME : SINGARA VENKATA SAI MEGHA
SHYAM
FATHER'S NAME : SINGARA SUBBA RAO
COURSE : B.TECH. [COMPUTER SCIENCE]

1ST SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------|--------------------------------|-----|----|---------|
| [BTCS-101] | ENGINEERING MATHEMATICS-I | 100 | 69 | PASS |
| [BTCS-102] | DATA STRUCTURE & ALGORITHMS | 100 | 68 | PASS |
| [BTCS-103] | VALUES & ETHICS IN PROFESSION | 100 | 70 | PASS |
| [BTCS-104] | COMPUTER ORGANIZATION | 100 | 64 | PASS |
| [BTCS-105] | NETWORK ANALYSIS AND SYNTHESIS | 100 | 69 | PASS |
| [BTCS-106] | DATA STRUCTURES LAB | 100 | 58 | PASS |

2ND SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------|--|-----|----|---------|
| [BTCS-201] | ENGINEERING MATHEMATICS-II | 100 | 74 | PASS |
| [BTCS-202] | PRINCIPLES OF PROGRAMMING LANGUAGE | 100 | 64 | PASS |
| [BTCS-203] | FORMAL LANGUAGE & AUTOMATA THEORY | 100 | 80 | PASS |
| [BTCS-204] | OPERATION RESEARCH & OPTIMIZATION TECHNIQUES | 100 | 66 | PASS |
| [BTCS-205] | PRINCIPLES OF COMMUNICATION ENGINEERING | 100 | 80 | PASS |
| [BTCS-206] | OPERATION RESEARCH LAB | 100 | 65 | PASS |

3RD SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------------|-----------------------------------|-----------|-----------|----------------|
| [BTCS-301] | MICROPROCESSOR & MICROCONTROLLERS | 100 | 74 | PASS |
| [BTCS-302] | ADVANCED COMPUTER ARCHITECTURE | 100 | 81 | PASS |
| [BTCS-303] | OPERATING SYSTEM | 100 | 72 | PASS |
| [BTCS-304] | DATABASE MANAGEMENT SYSTEM | 100 | 67 | PASS |
| [BTCS-305] | DESIGN & ANALYSIS OF ALGORITHM | 100 | 74 | PASS |
| [BTCS-306] | OPERATING SYSTEM LAB | 100 | 83 | PASS |

4TH SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------------|-----------------------------------|-----------|-----------|----------------|
| [BTCS-401] | CONTROL SYSTEM | 100 | 71 | PASS |
| [BTCS-402] | DIGITAL SYSTEMS | 100 | 51 | PASS |
| [BTCS-403] | NETWORKS ANALYSIS & SYNTHESIS - I | 100 | 74 | PASS |
| [BTCS-404] | COMPUTER NETWORK | 100 | 60 | PASS |
| [BTCS-405] | SOFTWARE ENGINEERING | 100 | 58 | PASS |
| [BTCS-406] | COMPUTER NETWORK LAB | 100 | 78 | PASS |

5TH SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------------|------------------------------------|-----------|-----------|----------------|
| [BTCS-501] | NETWORKS ANALYSIS & SYNTHESIS - II | 100 | 65 | PASS |
| [BTCS-502] | COMPUTER GRAPHICS & MULTIMEDIA | 100 | 66 | PASS |
| [BTCS-503] | SYSTEM SOFTWARE AND ADMINISTRATION | 100 | 61 | PASS |
| [BTCS-504] | AUTOMATIC CONTROL SYSTEM | 100 | 76 | PASS |
| [BTCS-505] | OBJECT TECHNOLOGY & UML | 100 | 74 | PASS |
| [BTCS-506] | OBJECT TECHNOLOGY LAB | 100 | 71 | PASS |

6TH SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------------|--|-----------|-----------|----------------|
| [BTCS-601] | LANGUAGE PROCESSOR | 100 | 66 | PASS |
| [BTCS-602] | ARTIFICIAL INTELLIGENCE | 100 | 61 | PASS |
| [BTCS-603] | VISUAL PROGRAMMING AND WEB TECHNOLOGY | 100 | 53 | PASS |
| [BTCS-604] | INDUSTRIAL MANAGEMENT | 100 | 62 | PASS |
| [BTCS-605] | ANALOG & DIGITAL COMMUNICATION ENGINEERING | 100 | 75 | PASS |
| [BTCS-606] | ARTIFICIAL INTELLIGENCE LAB | 100 | 74 | PASS |

7TH SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------------|---------------------------------------|-----------|-----------|----------------|
| [BTCS-701] | VISUAL PROGRAMMING AND WEB TECHNOLOGY | 100 | 55 | PASS |
| [BTCS-702] | FINANCIAL MANAGEMENT AND ACCOUNTS | 100 | 59 | PASS |
| [BTCS-703] | DISTRIBUTED DATABASE | 100 | 73 | PASS |
| [BTCS-704] | BIO INFORMATICS | 100 | 70 | PASS |
| [BTCS-705] | PARALLEL PROGRAMMING | 100 | 70 | PASS |
| [BTCS-706] | ASSIGNED PROJECT | 100 | 62 | PASS |

8TH SEMESTER

| UNIT CODE | UNIT TITLE | MM | MO | REMARKS |
|------------------|---------------------------|-----------|-----------|----------------|
| [BTCS-801] | ADVANCED OPERATING SYSTEM | 100 | 64 | PASS |
| [BTCS-802] | COMPUTATIONAL GEOMETRY | 100 | 60 | PASS |
| [BTCS-803] | COMPUTER NETWORKS | 100 | 65 | PASS |
| [BTCS-804] | MODELING & SIMULATION | 100 | 69 | PASS |
| [BTCS-805] | IMAGE PROCESSING | 100 | 67 | PASS |
| [BTCS-806] | VLSI LAB | 100 | 66 | PASS |

| | | |
|--------------|---------------------------|----------------|
| FINAL RESULT | GRAND TOTAL - 3254 / 4800 | FIRST DIVISION |
|--------------|---------------------------|----------------|

Dated :

Controller of Examination