

Transcript

ENROLLMENT NO. : MU2225B396987
STUDENT NAME : VIPIN PAL
FATHER'S NAME : RAM BAHADUR
COURSE : B.TECH. [COMPUTER SCIENCE] L

1ST SEMESTER

UNIT CODE	UNIT TITLE	MM	MO	REMARKS
[BTCS-301]	MICROPROCESSOR & MICROCONTROLLERS	100	71	PASS
[BTCS-302]	ADVANCED COMPUTER ARCHITECTURE	100	70	PASS
[BTCS-303]	OPERATING SYSTEM	100	73	PASS
[BTCS-304]	DATABASE MANAGEMENT SYSTEM	100	60	PASS
[BTCS-305]	DESIGN & ANALYSIS OF ALGORITHM	100	60	PASS
[BTCS-306]	OPERATING SYSTEM LAB	100	65	PASS

2ND SEMESTER

UNIT CODE	UNIT TITLE	MM	MO	REMARKS
[BTCS-401]	CONTROL SYSTEM	100	76	PASS
[BTCS-402]	DIGITAL SYSTEMS	100	70	PASS
[BTCS-403]	NETWORKS ANALYSIS & SYNTHESIS - I	100	69	PASS
[BTCS-404]	COMPUTER NETWORK	100	70	PASS
[BTCS-405]	SOFTWARE ENGINEERING	100	56	PASS
[BTCS-406]	COMPUTER NETWORK LAB	100	58	PASS

3RD SEMESTER

UNIT CODE	UNIT TITLE	MM	MO	REMARKS
[BTCS-501]	NETWORKS ANALYSIS & SYNTHESIS - II	100	74	PASS
[BTCS-502]	COMPUTER GRAPHICS & MULTIMEDIA	100	77	PASS
[BTCS-503]	SYSTEM SOFTWARE AND ADMINISTRATION	100	55	PASS
[BTCS-504]	AUTOMATIC CONTROL SYSTEM	100	78	PASS
[BTCS-505]	OBJECT TECHNOLOGY & UML	100	59	PASS
[BTCS-506]	OBJECT TECHNOLOGY LAB	100	70	PASS

4TH SEMESTER

UNIT CODE	UNIT TITLE	MM	MO	REMARKS
[BTCS-601]	LANGUAGE PROCESSOR	100	67	PASS
[BTCS-602]	ARTIFICIAL INTELLIGENCE	100	55	PASS
[BTCS-603]	VISUAL PROGRAMMING AND WEB TECHNOLOGY	100	79	PASS
[BTCS-604]	INDUSTRIAL MANAGEMENT	100	73	PASS
[BTCS-605]	ANALOG & DIGITAL COMMUNICATION ENGINEERING	100	63	PASS
[BTCS-606]	ARTIFICIAL INTELLIGENCE LAB	100	71	PASS

5TH SEMESTER

UNIT CODE	UNIT TITLE	MM	MO	REMARKS
[BTCS-701]	VISUAL PROGRAMMING AND WEB TECHNOLOGY	100	65	PASS
[BTCS-702]	FINANCIAL MANAGEMENT AND ACCOUNTS	100	78	PASS
[BTCS-703]	DISTRIBUTED DATABASE	100	64	PASS
[BTCS-704]	BIO INFORMATICS	100	79	PASS
[BTCS-705]	PARALLEL PROGRAMMING	100	54	PASS
[BTCS-706]	ASSIGNED PROJECT	100	70	PASS

6TH SEMESTER

UNIT CODE	UNIT TITLE	MM	MO	REMARKS
[BTCS-801]	ADVANCED OPERATING SYSTEM	100	70	PASS
[BTCS-802]	COMPUTATIONAL GEOMETRY	100	62	PASS
[BTCS-803]	COMPUTER NETWORKS	100	74	PASS
[BTCS-804]	MODELING & SIMULATION	100	65	PASS
[BTCS-805]	IMAGE PROCESSING	100	66	PASS
[BTCS-806]	VLSI LAB	100	68	PASS

FINAL RESULT	GRAND TOTAL - 2434 / 3600	FIRST DIVISION
---------------------	----------------------------------	-----------------------

Dated :**Controller of Examination**